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ABSTRACT OF THE DISCLOSURE

Receiver circuits are provided for detecting a target light source. The circuits include a photocell arranged to detect light impinging thereupon, and a biasing circuit that maintains a constant reverse voltage across the photocell irrespective of the photocell output. This maintains the capacitance of the photocell at a substantially constant value, and results in faster response times from the photocell. Further, the present invention provides circuits for effectively removing ambient conditions such as daylight from the output. Also, the present invention reduces the amount of signal loss between the photocell output and an amplifier due to loading at the input stage of the amplifier.